

ISSUE CLASSIFICATION		PATENT NUMBER	
Examiner	Patent Number	Issue Date	Patent Number
U.S. UTILITY Patent Application			
D.I.P.E.		PATENT DATE	
<i>AK</i> SCANNED <i>Ans</i>		<i>10/10/00</i> O.A. <i>LA</i>	
APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS
09/715748	F	438	237
ART UNIT	EXAMINER		
2842 2518	<i>Berry</i>		
APPLICANTS	Jean-Michel Reynes Ivana Deram Evgeniy Stefancov		
TITLE	Method of forming a diode for integration with a semiconductor device and method of forming a transistor device having an integrated diode		
PTO-2040 12/99			

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	1st Pmt/Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					
				ISSUE FEE Amount Due _____ Date Paid _____	
				ISSUE BATCH NUMBER	
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